Translation and Runtime System-based Techniques for Hiding the Cost of Communication

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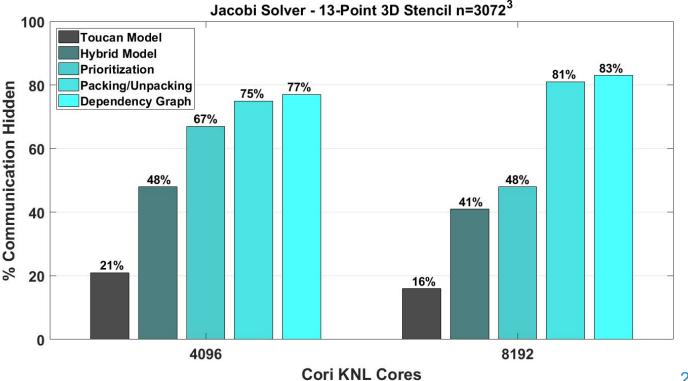


Roadmap (2017)

Progression of the % of communication hidden.

Starting from January's version of our translator (21%) until today (83%).

Each bar represents an incremental optimization.



Motivation

- Problem: Communication costs are significant in large-scale parallel applications
 Moreover, the overheads are continuing to grow towards the Exascale.
- Coping strategies:
 - Hiding Strategy: Overlap communication with computation
 - Avoiding Strategy: Performing less and/or more efficient communication
- Shortfalls of implementing coping strategies manually:
 - They may require algorithmic changes.
 - Entangles the coping strategy with the application logic.
 - For some large applications, these transformations are inviable.

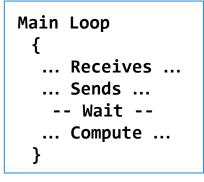
Overlap Communication and Computation via

Manual Transformation

Anatomy of a (Typical) MPI Program

Begin

Initialize Data



Other Communication Output Result Begin

Initialize Data

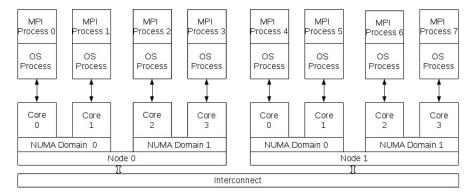
Main Loop

```
{
    ... Receives ...
    ... Sends ...
    ... Compute(Independent) ...
        -- Wait --
    ... Compute(Dependent) ...
}
```

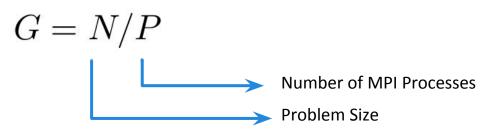
```
Other Communication
Output Result
```

Granularity of MPI Programs

Observation I: MPI programs *typically* reach optimal performance when *P* = *c*, where c = number of cores.



Equation: Granularity of an SPMD program. Defines how the workload is divided among processes.



Drawback: *P* = *c* fixes granularity to the underlying architecture and does not allow *oversubscription*.

Toucan Model

Introducing Toucan

- A Source-to-Source Translator of C/C++ MPI Applications.
 - Automatically generates an overlapping version of the source code.
 - Built using the ROSE Compiler Framework (LLNL).
 - Uses Bamboo's¹ annotation scheme.

• The translated code is linked to execute on our runtime system: MATE.

- MATE encapsulates all dynamic scheduling logic.
- Avoids code bloating compared to Bamboo (static scheduling)
- Supports recursive code.

• Toucan/MATE rely on two mechanisms:

- 1. Oversubscription of processor cores.
- 2. Splitting source code into code regions, scheduled individually.



(Pronounced 'Mah-tay')

Definitions

• Overdecomposition (SPMD):

 The problem domain is split into more partitions (Implies communication. i.e. ghost cells) than useful cores in the system.

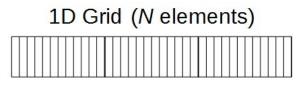
• Oversubscription:

 Instantiating more autonomously executing tasks (ranks) than useful cores at all times during the execution.

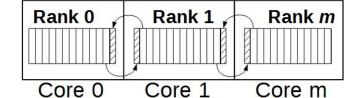
• Virtualization Factor:

- The integer multiplier by which oversubscription is achieved.
- E.g. Instantiating 64 ranks in a 32-core run \Rightarrow VF=2

Toucan Decomposition

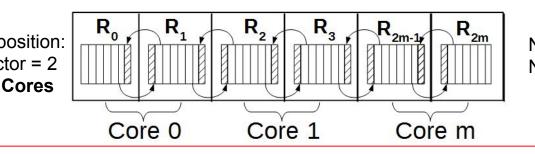


Typical Decomposition: Virtualization Factor = 1 **m Ranks, m Cores**



N/m Elements per Rank N/m Elements per Core

Toucan Decomposition: Virtualization Factor = 2 **2m Ranks, m Cores**



N/(2m) Elements per Rank N/m Elements per Core

Toucan can only achieve oversubscription through overdecomposition.

Code Regions (1/2): A Basic MPI Code

All operations are waited for at MPI_Waitall().

1 Task Exit Point: MPI_Waitall().

```
for (int i = 0; i < niterations; i++)
{
    MPI_Irecv(&Un[y][x], size.x, MPI_DOUBLE, WestRank, ...);
    MPI_Irecv(&Un[y][x], size.x, MPI_DOUBLE, EastRank, ...);
    MPI_Isend(&Un[y][x], size.x, MPI_DOUBLE, EastRank);
    MPI_Isend(&Un[y][x], size.x, MPI_DOUBLE, WestRank);</pre>
```

```
MPI_Waitall(MPIRequests);
```

Compute(); Swap(&U, &Un);

}

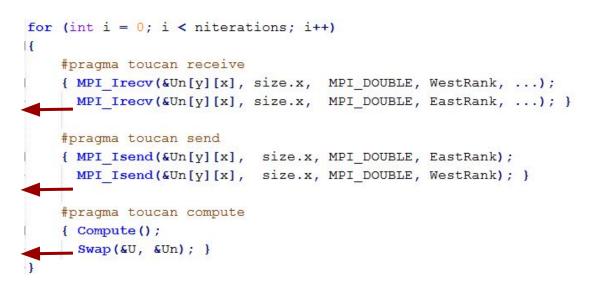
Code Regions (2/2): Using Toucan

3 pragma directives: (Compute, Send, Receive)

Dependency Graph implicitly defined by the Bamboo/Toucan model:

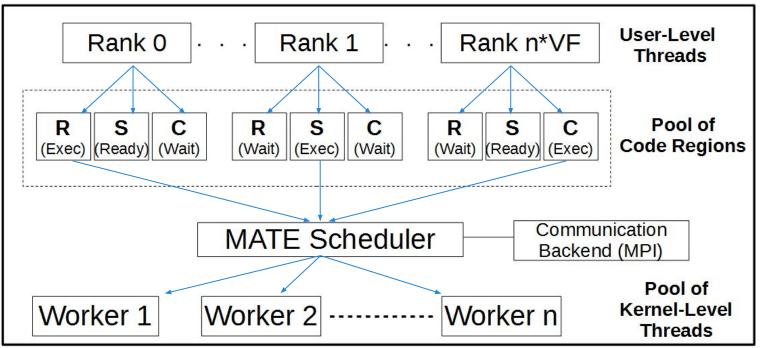
- Receive depends on compute*
- Send depends on compute* and send*
- Compute depends on receive ops only

3 Task Exit Points: One at the start/end of each region.

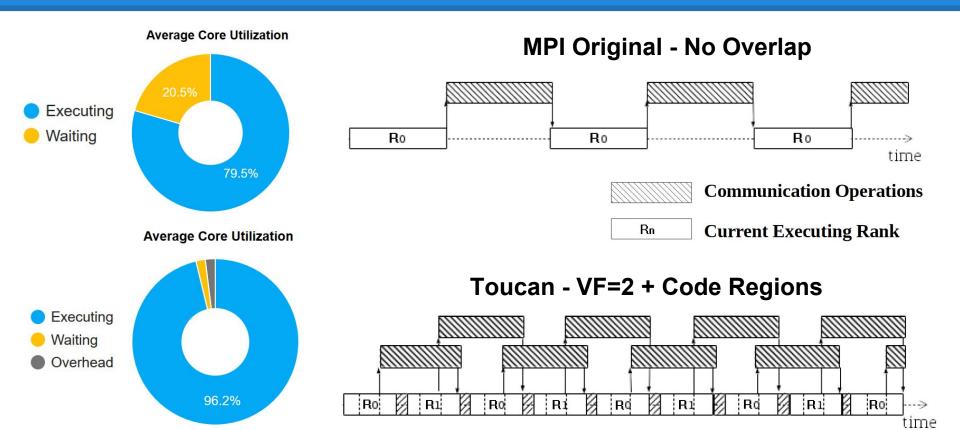


Runtime System (MATE/Toucan)

Mate Process



Core Usage Timeline



Hardware Testbed: Cori KNL @ NERSC

Node Configuration:

• 1 x 68-core Intel KNL processor @ 1.4 Ghz (We only use 64 cores per Node)

Memory:

- 16 GB MCDRAM ~ 460 GB/s
- 96 GB DDR4 ~102 GB/s

Software:

- Cray-MPICH v7.6.0
- Intel icc compiler 17.0.2 (-O3)

Test Case:

- 13-Point Stencil 3D Jacobi Solver
- 2 Control Variants:
 - MPI Original
 - Upper Bound

MPI Original without communication, just synchronization

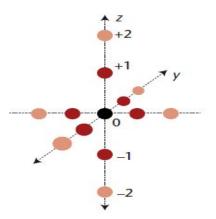


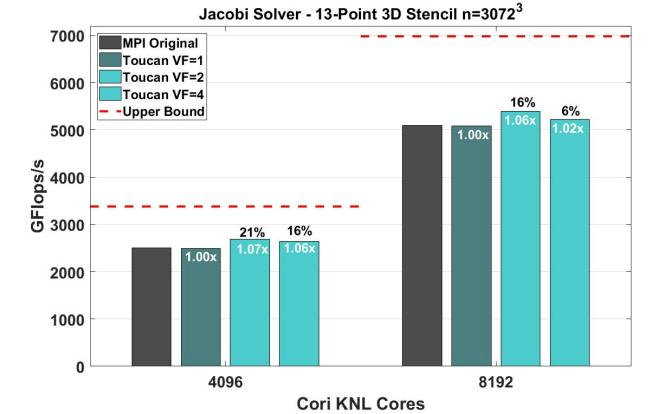
Image Source: "Accelerating a 3D Finite-Difference Earthquake Simulation with a C-to-CUDA Translator", Cai et al.

Results: Toucan Variants

VF=1 Does not allow any overlap since cores cannot find ready ranks while other communicate.

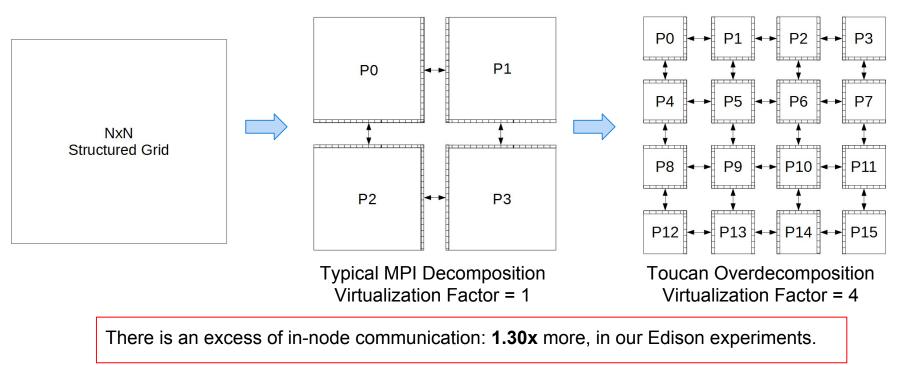
VF=2 Achieves optimal performance in both cases.

VF=4 Loses performance due to the surface/volume ratio problem.



Surface/Volume Ratio Problem

- Overdecomposition in Toucan requires communicating extra ghost cells.
- Suffers from increased in-node communication.

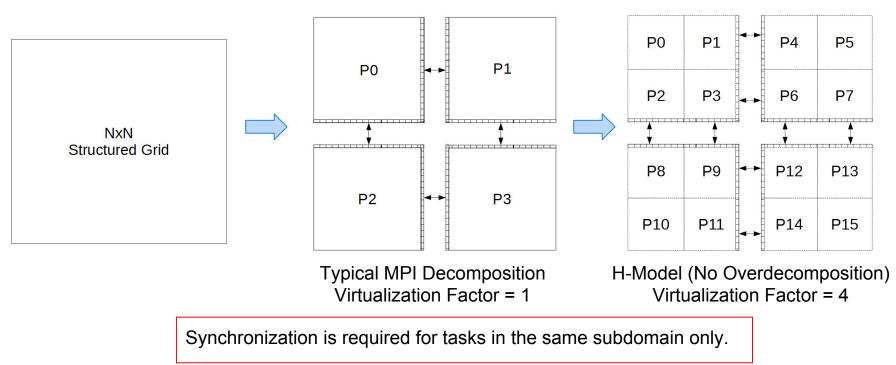


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Hybrid Model

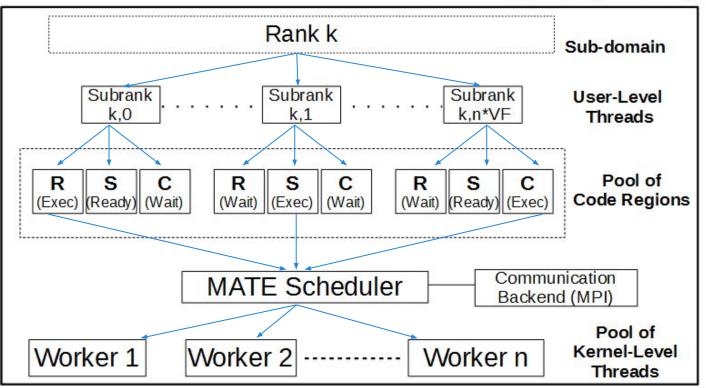
MATE Hybrid Model

- Problem domain is not overdecomposed.
- Instead, oversubscribed tasks share the same process-wide subdomain.



Runtime System (MATE/Hybrid)

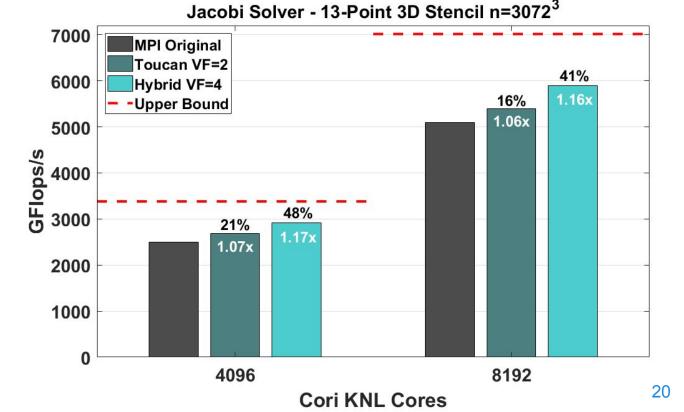
Mate Process (Node k)



Results: Hybrid vs Toucan

Since the Hybrid model does not increase the amount of communication inside a node, higher Virtualization Factors can be used.

In this case, **VF=4** is the ideal setting for the Hybrid Variant, achieving more than double the overlap yield of the Toucan variant.

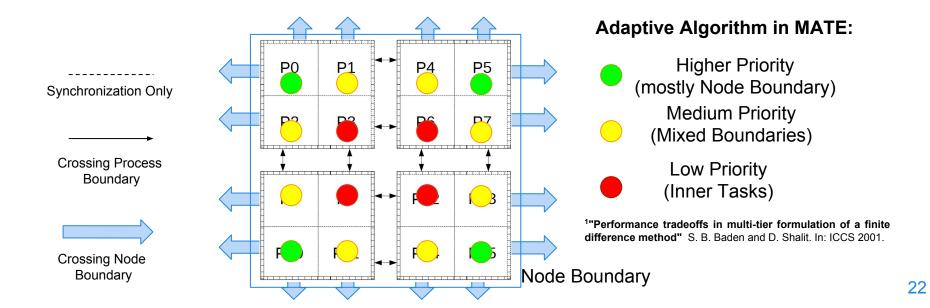


Optimization #1: Subrank

Prioritization

Subrank Prioritization

- Fact: Not all subranks incur the same communication cost.
- Idea¹: Prioritize subranks with higher communication cost to execute first.
- Effect: Maximize computation while longer communication is performed.



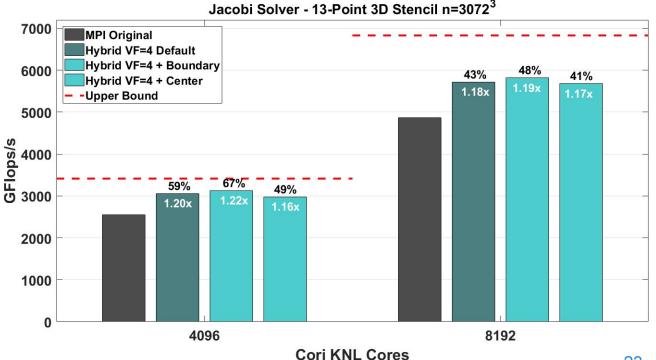
Results: Prioritization

We tested 3 prioritization schemes:

Hybrid VF=4 Default No priority scheme. Execution order is mostly random.

Hybrid VF=4 + Boundary Priority scheme as described in the previous slide.

Hybrid VF=4 + Center The opposite scheme than the one described in the previous slide.



Optimization #2: Thread Concurrency

Thread Concurrency Problem

- MATE processes currently use MPI as communication backend.
- MPI implements a process-wide lock, which limits communication concurrency.
- Non-Contiguous Datatypes are particularly problematic:

MPI_Isend(strided_type)

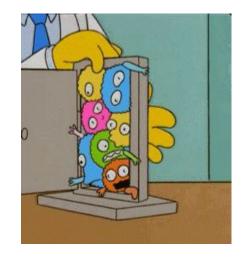
Pack strided data into a hidden contiguous buffer.
 Transmit buffered data to destination rank.

MPI_Lock

MPI_Irecv(strided_type)

Receive incoming data into a hidden contiguous buffer.
 Unpack data into destination with strides.





Thread Concurrency Problem

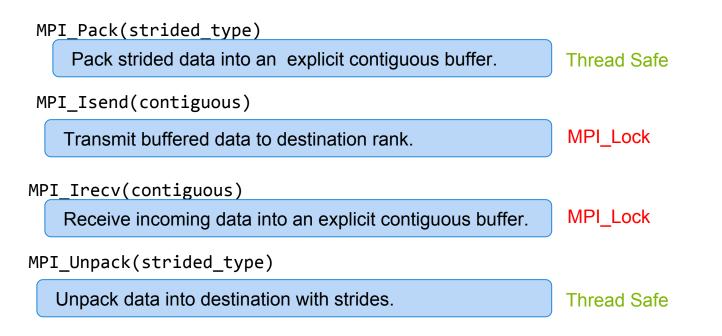
- A way to cope with this problem is to split MPI operations:
 - MPI_Isend \Rightarrow MPI_Pack + MPI_Isend (contiguous)
 - MPI_Irecv ⇒ MPI_Irecv (contiguous) + MPI_Unpack

```
MPI_Isend(&U[z][y][x], 1, faceZ_type, ...);
MPI_Isend(&U[z][y][x], 1, faceZ_type, ...);
MPI_Isend(&U[z][y][x], 1, faceX_type, ...);
MPI_Isend(&U[z][y][x], 1, faceX_type, ...);
MPI_Isend(&U[z][y][x], 1, faceY_type, ...);
MPI_Isend(&U[z][y][x], 1, faceY_type, ...);
```

```
MPI Pack(&U[z][y][x], 1, faceZ type, downSendBuffer[d], ...);
MPI Pack(&U[z][y][x], 1, faceZ type, upSendBuffer[d], ...);
MPI Pack(&U[z][y][x], 1, faceX type, eastSendBuffer[d], ...);
MPI Pack(&U[z][y][x], 1, faceX type, westSendBuffer[d], ...);
MPI Pack(&U[z][y][x], 1, faceY type, northSendBuffer[d], ...);
MPI Pack(&U[z][y][x], 1, faceY type, southSendBuffer[d], ...);
MPI Isend (downSendBuffer[d],
                              side.x * side.y, MPI DOUBLE, ...);
MPI Isend(upSendBuffer[d],
                              side.x * side.y, MPI DOUBLE, ...);
MPI Isend(eastSendBuffer[d],
                              side.y * side.z, MPI DOUBLE, ...);
MPI Isend (westSendBuffer[d],
                              side.y * side.z, MPI DOUBLE, ...);
MPI Isend (northSendBuffer [d], side.x * side.z, MPI DOUBLE, ...);
MPI Isend(southSendBuffer[d], side.x * side.z, MPI DOUBLE, ...);
```

Thread Concurrency Problem

After splitting the operations, threads can perform concurrent packing/unpacking.

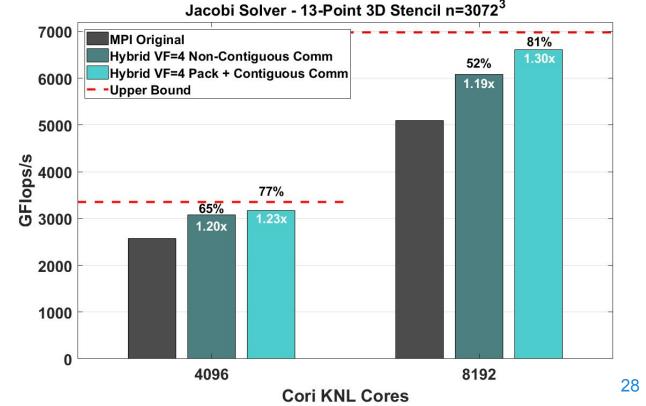


Results: Contiguous vs Non-Contiguous

Comparing 2 Variants:

Hybrid VF=4 + Non-Contiguous Communication

Hybrid VF=4 + Packing/Unpacking + Contiguous Communication



Optimization #3: Dependency Graph Refinement

Toucan-Annotated Program

3 pragma annotations: (Compute, Send, Receive)

Dependency Graph implicitly defined

- Receive depends on compute*
- Send depends on compute* and send*
- Compute depends on receive ops only

```
3 Task Exit Points.
```

```
for (int i = 0; i < niterations; i++)</pre>
   #pragma toucan receive
    { MPI Irecv(eastRecvBuffer[d], count east, faceX type, eastRecvBuffer[d], ...);
     MPI Irecv(eastRecvBuffer[d], count west, faceX type, westRecvBuffer[d], ...); }
    #pragma toucan send
    { MPI Pack(&Un[z][y][x], count east, faceX type, eastSendBuffer[d], ...);
     MPI Pack(&Un[z][y][x], count west, faceX type, westSendBuffer[d], ...);
     MPI Isend(eastSendBuffer[d], size.y*size.z, MPI DOUBLE, EastRank);
     MPI Isend (westSendBuffer[d], size.y*size.z, MPI DOUBLE, WestRank); }
    #pragma toucan compute
    [ MPI Unpack(&U[z][y][x],
                               size.y*size.z, MPI DOUBLE, EastRank);
     MPI Unpack(&U[z][y][x],
                               size.y*size.z, MPI DOUBLE, WestRank);
     Compute();
     Swap(&U, &Un); }
```

Explicit Graph in MATE

5 pragma annotations: (Compute, Pack, Send, Receive, Unpack)

Dependency Graph explicitly defined

- Receive depends on Unpack*
- Pack depends on Compute*, Send*
- Send depends on Pack
- Unpack depends on Receive
- Compute depends on Unpack

5 Task Exit Points.

```
for (int i = 0; i < niterations; i++)</pre>
    #pragma mate region(receive) depends (compute*)
    { MPI Irecv(eastRecvBuffer[d], count east, faceX type, eastRecvBuffer[d], ...);
     MPI Irecv(eastRecvBuffer[d], count west, faceX type, westRecvBuffer[d], ...); }
    #pragma mate region(pack) depends (compute*, send*)
    { MPI Pack(&Un[z][y][x], count east, faceX type, eastSendBuffer[d], ...);
     MPI Pack(&Un[z][y][x], count west, faceX type, westSendBuffer[d], ...); }
    #pragma mate region(send) depends (pack)
    { MPI Isend(eastSendBuffer[d], size.y*size.z, MPI DOUBLE, EastRank);
    MPI Isend(westSendBuffer[d], size.v*size.z, MPI DOUBLE, WestRank); }
    #pragma mate region(unpack) depends (receive)
    { MPI Unpack(&U[z][y][x], size.y*size.z, MPI DOUBLE, EastRank);
     MPI Unpack(&U[z][y][x], size.y*size.z, MPI DOUBLE, WestRank); }
    #pragma mate region(compute) depends (unpack)
      Compute();
      Swap(&U, &Un); }
```

*From previous iteration

1

Directional Annotations

9 pragma annotations: (Pack, Send, Receive, Unpack) x 2 Compute

Dependency Graph explicitly defined

All regions and dependencies are split into their specific directions

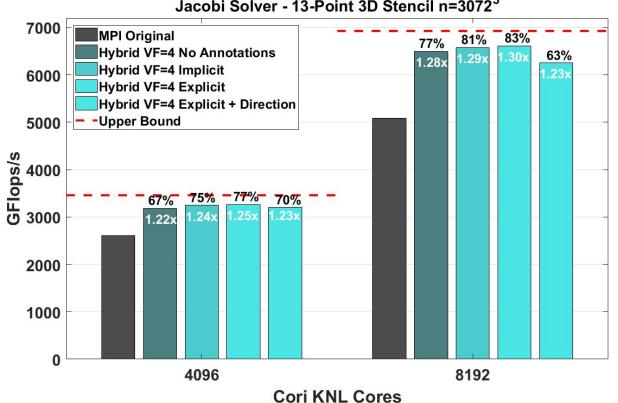
9 Task Exit Points.

*From previous iteration

```
for (int i = 0; i < niterations; i++)</pre>
    #pragma mate region(receive east) depends (compute*)
     MPI Irecv(eastRecvBuffer[d], count east, faceX type, eastRecvBuffer[d], ...);
    #pragma mate region(receive west) depends (compute*)
     MPI Irecv(eastRecvBuffer[d], count_west, faceX_type, westRecvBuffer[d], ...);
    #pragma mate region(pack east) depends (compute*, send east*)
     MPI Pack(&Un[z][y][x], count east, faceX type, eastSendBuffer[d], ...);
    #pragma mate region(pack west) depends (compute*, send west*)
     MPI Pack(&Un[z][y][x], count west, faceX type, westSendBuffer[d], ...);
    #pragma mate region(send west) depends (pack east)
     MPI Isend (eastSendBuffer[d], size.y*size.z, MPI DOUBLE, EastRank);
    #pragma mate region(send east) depends (pack west)
     MPI Isend (westSendBuffer[d], size.y*size.z, MPI DOUBLE, WestRank);
    #pragma mate region(unpack east) depends (receive east)
     MPI Unpack(&U[z][y][x], size.y*size.z, MPI DOUBLE, EastRank);
    #pragma mate region(unpack west) depends (receive west)
     MPI Unpack(&U[z][y][x], size.y*size.z, MPI DOUBLE, WestRank);
    #pragma mate region(compute) depends (unpack east, unpack west)
    { Compute();
     Swap (&U, &Un); }
```

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Results: Dependency Graph Variants



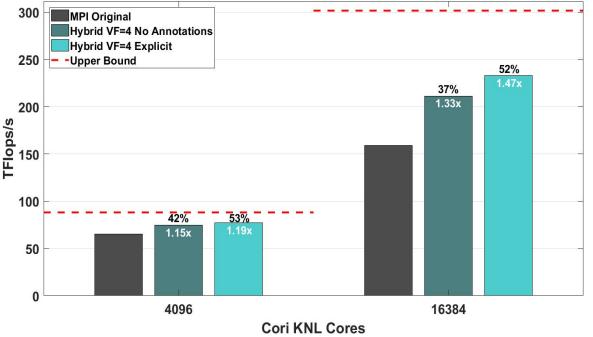
Jacobi Solver - 13-Point 3D Stencil n=3072³

Results: Matrix Multiplication

Results for the Cannon2D Matrix Multiply algorithm

Comparing variants with and without annotations.

The variant with an explicitly defined graph hides 11-15% more of the communication cost.



Cannon2D Matrix Multiplication - Weak Scaling

Conclusions

Conclusions

- The Hybrid Model exceeds the efficiency of the Toucan Model
 - Hides communication by Oversubscription+Regions (like Toucan) but,
 - It does not require overdecomposition (zero added communication).
- Subrank prioritization can have a substantial impact on performance
 - Communication cost is not homogeneous, thus creating opportunities for further communication/computation overlap.
 - The MATE runtime can assign priorities automatically during execution.
- Thread concurrency is still an important issue to be solved
 - Even if packing can be performed concurrently, MPI still locks comm ops.
- It is possible to refine dependency graphs explicitly

Future Work

- Apply the Hybrid Model on real-world applications / benchmarks
 E.g. Mpix_FlowCart (>20k LoC)
- Replace MATE's communication layer.
 - Use a thread-tolerant communication layer (e.g. GASNetEx, UPC++)
 - While keeping the current MPI-2 programming interface.
- Use parallel profiling tools to examine the low-level effects of our models
 O HPCToolkit
- Explore the effect of refining code regions and dependencies
 Their impact on performance needs to be investigated further.

Questions?

For an explanation of Toucan's rationale and experimental results on the Edison supercomputer, check our IPDPS'17 paper:

"Toucan - A Translator for Communication Tolerant MPI Applications" S. Martin, M. J. Berger, and S. B. Baden